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Message :

U.S.S.N.: 09/851,885
Inventors: Kubota, et al.
Entitled: DATA SIGNAL LINE DRIVE CIRCUIT, IMAGE DISPLAY DEVICE INCORPORATING THE SAME, AND ELECTRONIC APPARATUS USING THE SAME
Group: 2673
Examiner: Kovalick, Vincent E.
Our Reference: 55845 (70904)

Resend with claims 11, 12, 13 added

Fax

Annotated version

2. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

only one of the parts and entireties of the drive circuit(s) operates at any given time.

3. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods.

4. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claim 1 or 122,~~

~~wherein:~~

two or more of the parts and entireties of the drive circuit(s) are switchably driven in one frame period.

5. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

at least two of the parts and entireties of the drive circuit(s) write image data in respective areas on a screen.

6. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least two of the parts and entireties of the data signal line drive circuit write image data in one partial or whole area on a screen in one frame period.

11. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data in a blanking period of each horizontal scan period.

12. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

13. (Currently Amended) The image display device as defined in claim 1, wherein: ~~claims 1 or 122,~~

~~wherein:~~

the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.

126. (New) The image display device as defined in claim 122, wherein:

only one of the parts and entireties of the drive circuit(s) operates at any given time.

127. (New) The image display device as defined in claim 122, wherein:

the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods.

128. (New) The image display device as defined in claim 122, wherein:

two or more of the parts and entireties of the drive circuit(s) are switchably driven in one frame period.

129. (New) The image display device as defined in claim 122, wherein:

at least two of the parts and entireties of the drive circuit(s) write image data in respective areas on a screen.

130. (New) The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least two of the parts and entireties of the data signal line drive circuit write image data in one partial or whole area on a screen in one frame period.

131. (New) The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data in a blanking period of each horizontal scan period.

132. (New) The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

133. (New) The image display device as defined in claim 122, wherein:

the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.

Clean Version

2. The image display device as defined in claim 1, wherein:

only one of the parts and entireties of the drive circuit(s) operates at any given time.

3. The image display device as defined in claim 1, wherein:

the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods.

4. The image display device as defined in claim 1, wherein:

two or more of the parts and entireties of the drive circuit(s) are switchably driven in one frame period.

5. The image display device as defined in claim 1, wherein:

at least two of the parts and entireties of the drive circuit(s) write image data in respective areas on a screen.

6. The image display device as defined in claim 1, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least two of the parts and entireties of the data signal line drive circuit write image data in one partial or whole area on a screen in one frame period.

11. The image display device as defined in claim 1, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data in a blanking period of each horizontal scan period.

12. The image display device as defined in claim 1, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

13. The image display device as defined in claim 1, wherein:

the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.

126. The image display device as defined in claim 122, wherein:

only one of the parts and entireties of the drive circuit(s) operates at any given time.

127. The image display device as defined in claim 122, wherein:

the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods.

128. The image display device as defined in claim 122, wherein:

two or more of the parts and entireties of the drive circuit(s) are switchably driven in one frame period.

129. The image display device as defined in claim 122, wherein:

at least two of the parts and entireties of the drive circuit(s) write image data in respective areas on a screen.

130. The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least two of the parts and entireties of the data signal line drive circuit write image data in one partial or whole area on a screen in one frame period.

131. The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data in a blanking period of each horizontal scan period.

132. The image display device as defined in claim 122, wherein:

a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

133. The image display device as defined in claim 122, wherein:

the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.